

L Number	Hits	Search Text	DB	Time stamp
-	5	picopower adj3 tech	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 09:13
-	2	5677849.pn.	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 09:17
-	2	5710881.pn.	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 09:20
-	63	(nguyen near3 tai).inv.	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 09:22
-	16	((nguyen near3 tai).inv.) and clock\$3	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 09:25
-	19	(harland near3 glenn near3 hopkins).inv.	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 09:27
-	67	(duy near3 nguyen).inv.	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 09:27
-	3	((duy near3 nguyen).inv.) and clock\$3.ab.	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 09:28
-	22	(kevin near3 mcgonagle).inv.	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 09:29
-	12	(victor near3 liu).inv.	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 09:29
-	2377	processors near5 shar\$3 adj3 (component or memory)	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 10:25
-	6793	selective\$2 near5 clock\$3	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 10:26
-	23	(processors near5 shar\$3 adj3 (component or memory)) and (selective\$2 near5 clock\$3)	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 10:38

	7099	disabl\$3 near5 clock\$3	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 10:39
	23	(processors near5 shar\$3 adj3 (component or memory)) and (disabl\$3 near5 clock\$3)	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 10:48
	8	(disabl\$3 near5 clock\$3) same shar\$3 adj3 (component or memory)	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:10
	895	processors same clock same components	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:10
	2	processors same clock same shared adj3 components	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:11
	26	processors same different adj3 clock same components	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:20
	0	acks same grant\$3 near5 access	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:21
	119	ack same grant\$3 near5 access	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:21
	1	ack same interfaces same grant\$3 near5 access	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:25
	10	acknowledge same interfaces same grant\$3 near5 access	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:28
	1	vot\$3 same redundan\$2 same grant\$3 near5 access	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:30
	128	redundan\$2 same grant\$3 near5 access	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:31
	0	all near5 acknowledge\$4 same grant\$3 near5 access	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:31
	0	each near5 acknowledge\$4 same grant\$3 near5 access	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:33

	67	receiv\$3 near5 acknowledge\$4 same grant\$3 near5 access	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:35
	2	receiv\$3 near5 acknowledgements same grant\$3 near5 access	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:35
	398	receiv\$3 near5 signals same grant\$3 near5 access	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:35
	30	acknowledge near5 signals same grant\$3 near5 access	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:41
	0	receiv\$3 near8 all near5 ack	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:41
	0	receiv\$3 near8 all near5 acknowledge	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:41
	0	receiv\$3 near8 all near5 acknowledgement	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:42
	0	receiv\$3 near8 each near5 acknowledgement	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:42
	0	receiv\$3 near8 each near5 acknowledge	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:43
	0	each near8 assert\$3 same grant\$3 near5 access	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:43
	0	all near8 assert\$3 same grant\$3 near5 access	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:46
	8443	bit\$1 same enabl\$3 near5 clock\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:46
	2700	bit\$1 near8 enabl\$3 near5 clock\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:46
	1	bit\$1 near8 enabl\$3 near5 clock\$3 near5 subsystem	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:47

	289	register near3 bit\$1 near8 enabl\$3 near5 clock\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:47
	73	register adj3 bit\$1 near8 enabl\$3 near5 clock\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 15:44
	225	"and" near5 enabl\$3 near3 bits	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 15:45
	14	"and" near5 enabl\$3 near3 bits near8 clock\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 15:54
	3329	"and" near5 enabl\$3 near8 clock\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 15:55
	304	"and" near5 signals near8 enabl\$3 near8 clock\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 15:55
	265	"and" near5 signals near5 enabl\$3 near8 clock\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 15:55
	222	"and" near5 signals near5 enabl\$3 near5 clock\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 15:56
	85	"and" near3 signals near5 enabl\$3 near5 clock\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 15:56
	65	"and" near3 signals near5 enabl\$3 near3 clock\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 16:09
	17	"or" near3 signals near5 enabl\$3 near3 clock\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 16:21
	165	"and" near5 ack	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 16:22
	35	"and" near5 ack near5 enabl\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 16:22
	7	"and" near5 ack near3 signals	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 16:24

	100	"and" near5 acknowledgement	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 16:24
	58	"and" near3 acknowledgement	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 16:26
	9	"and" near3 acknowledge near3 signals	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 16:26
	7	processors same different near3 clocks same shared	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/21 08:57
	1	clock near3 tree same processors same shared	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/21 08:58
	30	clock near3 tree same integrated same components	USPAT; US-PCPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/21 12:21